



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

For application of: Michael J. Branson et al. : Date: February 7, 2002
#7
Group Art Unit: Unassigned : IBM Corporation
Examiner: Unassigned : Intellectual Property Law
Serial No.: 10/051,405 : Dept. 917, Bldg. 006-1
Filed: January 18, 2002 : 3605 Highway 52 North
Title: APPARATUS AND METHOD FOR
SYNCHRONIZING SOFTWARE
BETWEEN COMPUTERS : Rochester, MN 55901

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

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FEB 25 2002

Technology Center 2100

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. §1.97. In accordance with 37 C.F.R. §1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standard of materiality defined in 37 C.F.R. §1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. §1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. §1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

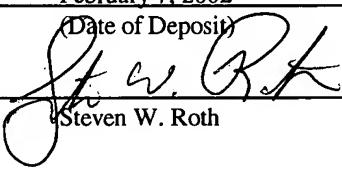
Serial No. 10/051,405
Docket No. ROC919980274US2

This Information Disclosure Statement is being filed under 37 C.F.R. §1.97(b)(1) within three months of the filing date of the present application. Accordingly no filing fee is required.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

February 7, 2002

(Date of Deposit)


Steven W. Roth

Respectfully submitted,

MICHAEL J. BRANSON ET AL.

By



Steven W. Roth

Attorney Reg. No. 34,712

IBM Corporation - Department 917

3605 Highway 52 North

Rochester, Minnesota 55901-7829

Telephone: 1-507-253-1600

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS
INFORMATION
DISCLOSURE STATEMENT
(Use several sheets if necessary)

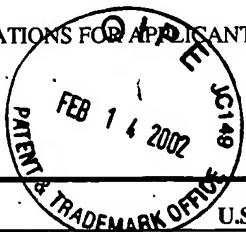
ATTY. DOCKET NO.
ROC919980274US2

SERIAL NO.
10/051,405

APPLICANT:
Michael J. Branson et al.

FILING DATE:
January 18, 2002

GROUP:
Unassigned



REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

| EXAMINER INITIALS | DOCUMENT NUMBER | | | | | | | DATE | NAME | CLASS | SUB-CLASS | FILING DATE (IF APPRO.) |
|-------------------|-----------------|---|---|---|---|---|---|----------|-----------------|-------|-----------|-------------------------|
| AA | 4 | 5 | 5 | 8 | 4 | 1 | 3 | 12/10/85 | Schmidt et al. | 364 | 300 | |
| AB | 5 | 4 | 1 | 0 | 7 | 0 | 3 | 04/25/95 | Nilsson et al. | 395 | 700 | |
| AC | 5 | 7 | 5 | 8 | 3 | 4 | 0 | 05/26/98 | Nail | 707 | 9 | |
| AD | 5 | 7 | 6 | 1 | 5 | 0 | 4 | 06/02/98 | Corrigan et al. | 395 | 652 | |
| AE | 5 | 8 | 6 | 7 | 7 | 1 | 4 | 02/02/99 | Todd et al. | 395 | 712 | |
| AF | 6 | 1 | 3 | 8 | 2 | 7 | 4 | 10/24/00 | Huang et al. | 717 | 11 | |
| AG | 6 | 2 | 0 | 5 | 5 | 7 | 9 | 03/20/01 | Southgate | 717 | 11 | |
| AH | | | | | | | | | | | | |
| AI | | | | | | | | | | | | |
| AJ | | | | | | | | | | | | |
| AK | | | | | | | | | | | | |

FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB-CLASS | TRANSLATION | |
|----|--|-----------------|------|---------|-------|-----------|-------------|----|
| | | | | | | | YES | NO |
| AL | | | | | | | | |
| AM | | | | | | | | |
| AN | | | | | | | | |
| AO | | | | | | | | |
| AP | | | | | | | | |

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

| | | |
|----|---|--|
| AR | Graunke et al., "Synchronization Algorithms for Shared-Memory Multiprocessors," IEEE - Computer, pp. 60-69, 1990. | |
| | | |
| AS | Hsiung, "Timing Coverification of Concurrent Embedded Real-Time Systems," ACM CODES, pp. 110-114, 1999. | |
| | | |
| AT | Agarwal et al., "Adaptive Backoff Synchronization Techniques," ACM, pp. 396-406, 1989. | |
| | | |

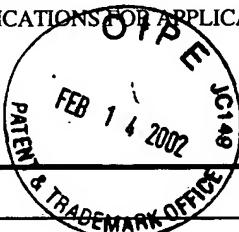
EXAMINER

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FORM PTO-1449 (Modified)

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INFORMATION
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ATTY. DOCKET NO.
ROC919980274US2SERIAL NO.
10/051,405APPLICANT:
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|-------------------|-----------------|------|------|-------|-----------|-------------------------|
| BA | | | | | | |
| BB | | | | | | |
| BC | | | | | | |
| BD | | | | | | |
| BE | | | | | | |
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|----|-----------------|------|---------|-------|-----------|-------------|----|
| | | | | | | YES | NO |
| BL | | | | | | | |
| BM | | | | | | | |
| BN | | | | | | | |
| BO | | | | | | | |
| BP | | | | | | | |

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

| | |
|----|---|
| BR | Laventhal, "A Constructive Approach to Reliable Synchronization Code," IEEE - CHI, pp. 194-202, 1979. |
| BS | |
| BT | |

EXAMINER

DATE CONSIDERED

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